

Figure 1

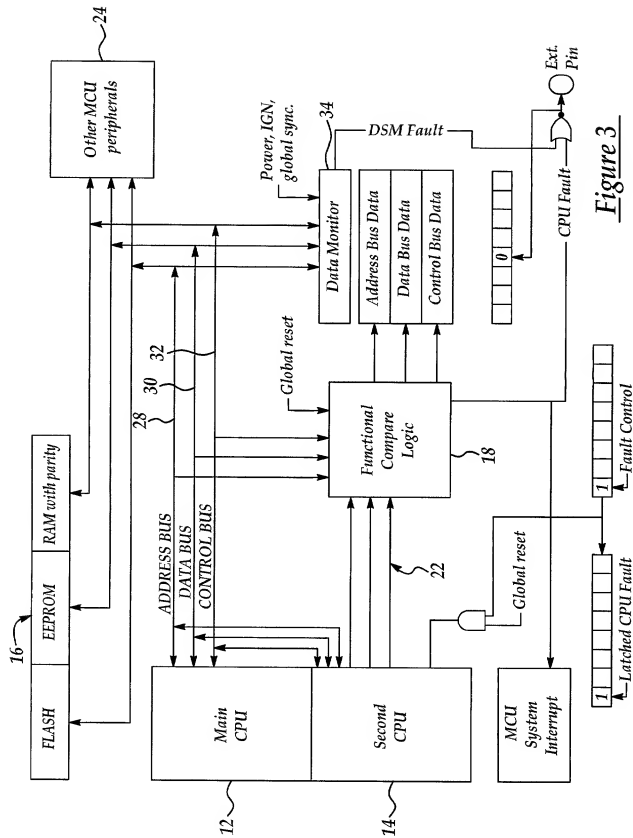


Figure 3

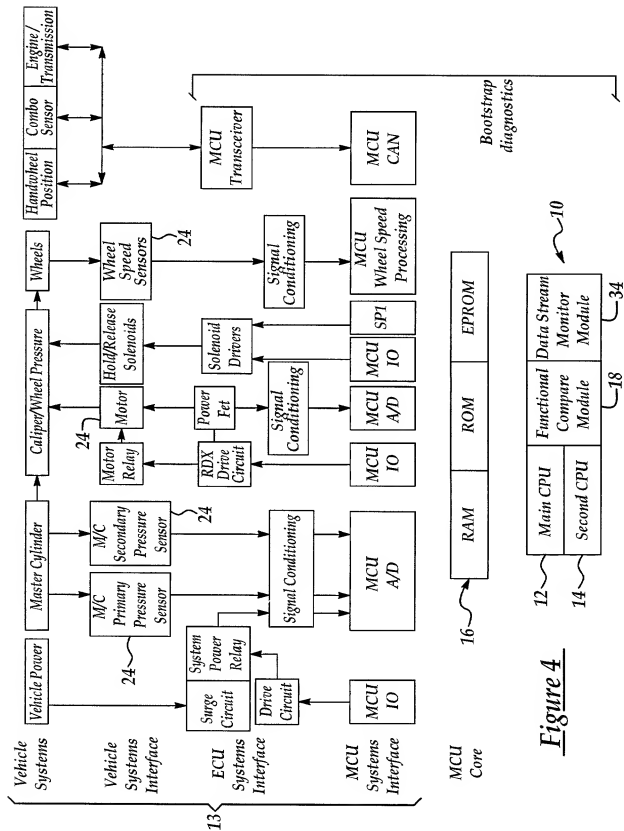


Figure 4

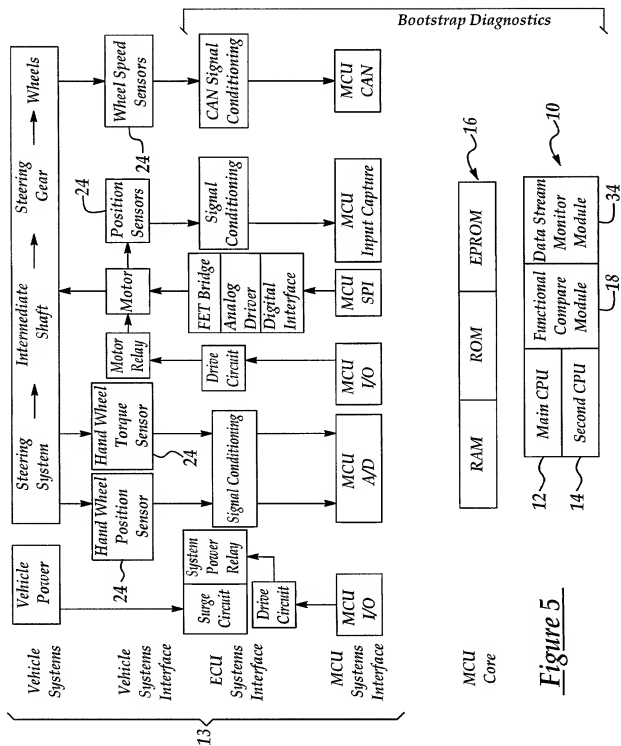
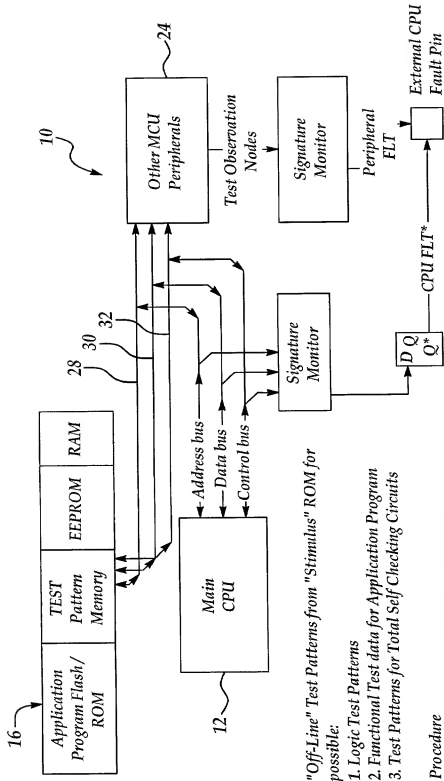


Figure 5



"Off-Line" Test Patterns from "Stimulus" ROM for possible:

1. Logic Test Patterns
2. Functional Test data for Application Program
3. Test Patterns for Total Self Checking Circuits

Procedure

1. Test Main CPU and Memory "Kernel" first
2. Test Peripherals second

Figure 6

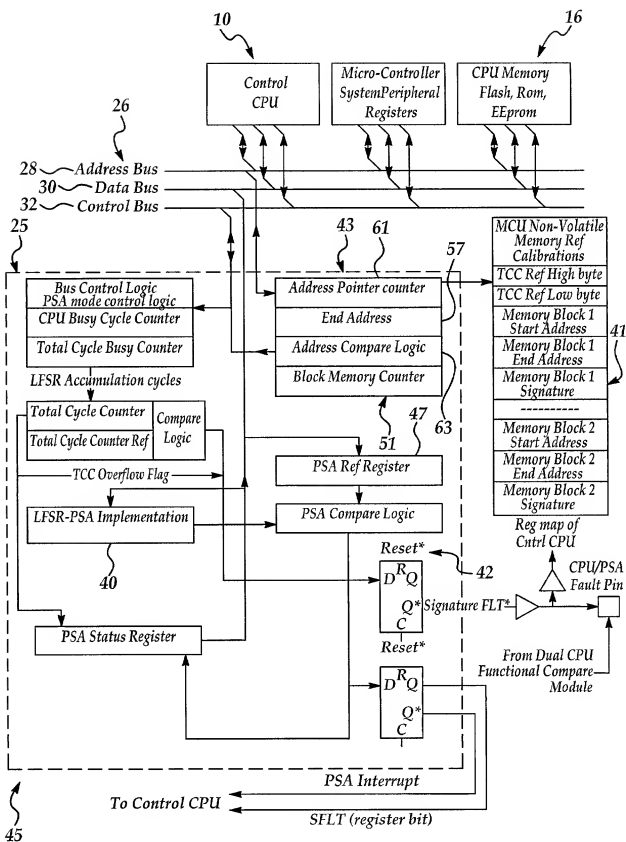


Figure 7

